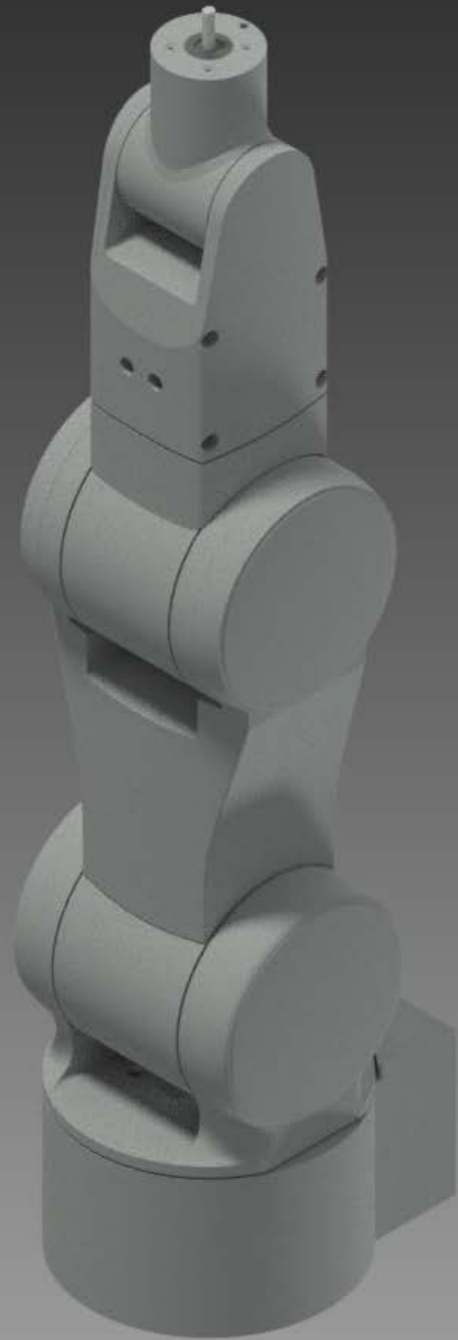


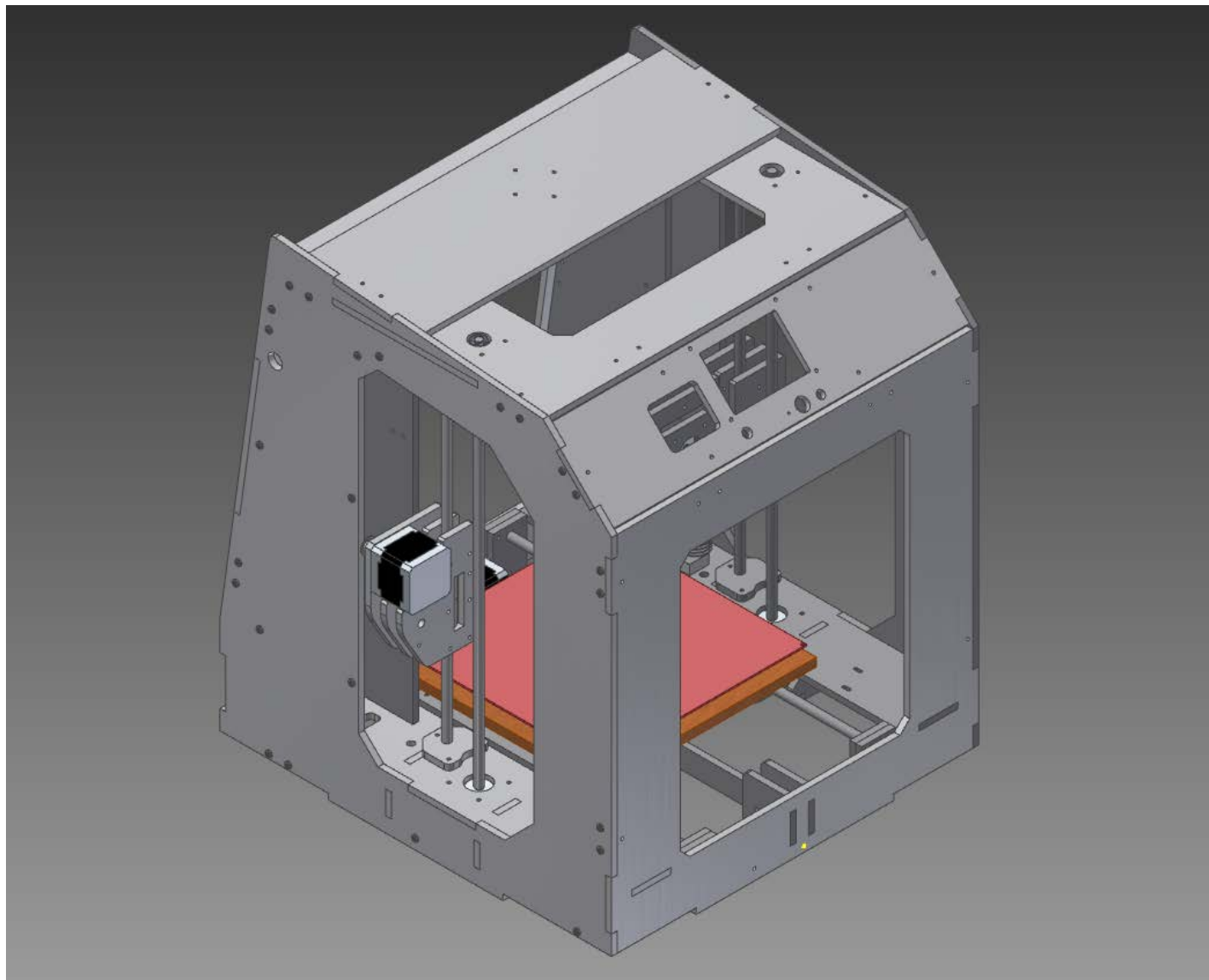
Настольный робот - манипулятор для лаборатории цифрового производства FAB LAB

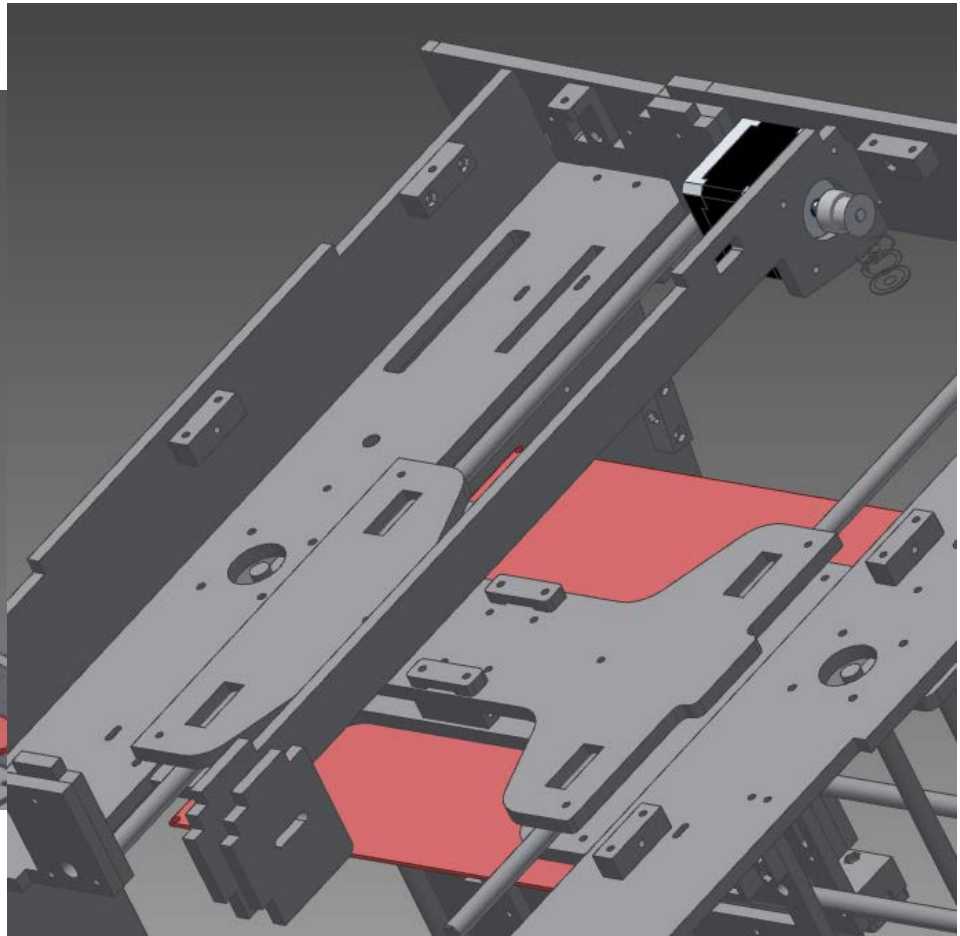
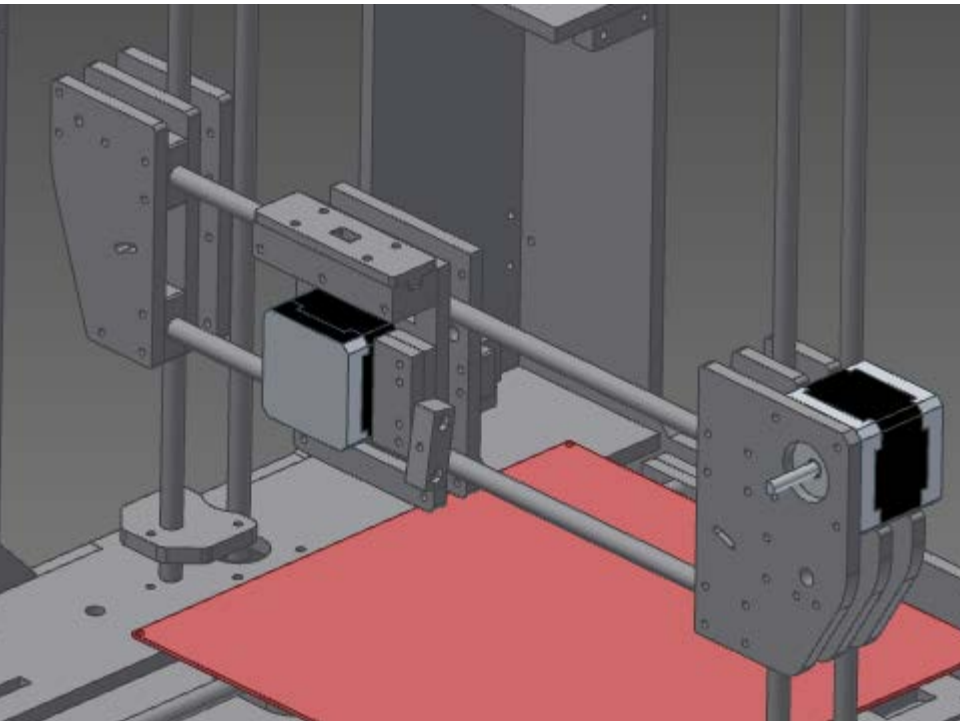
Автор: Зобов Олег

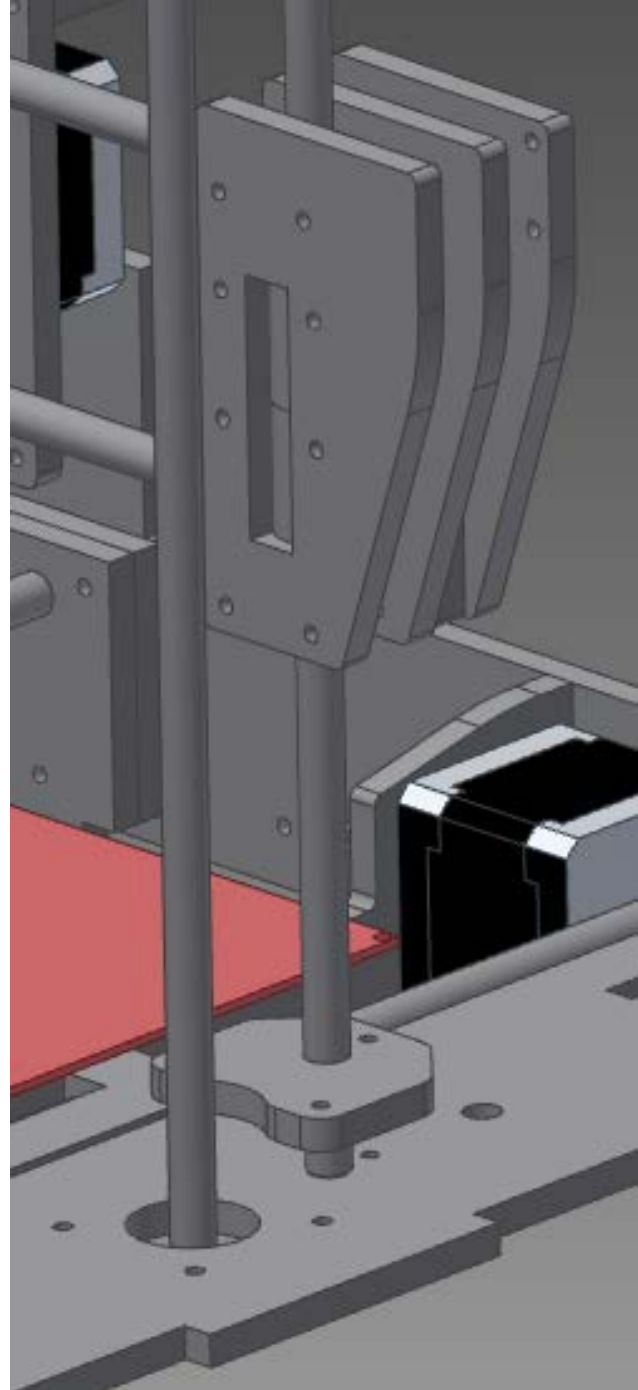
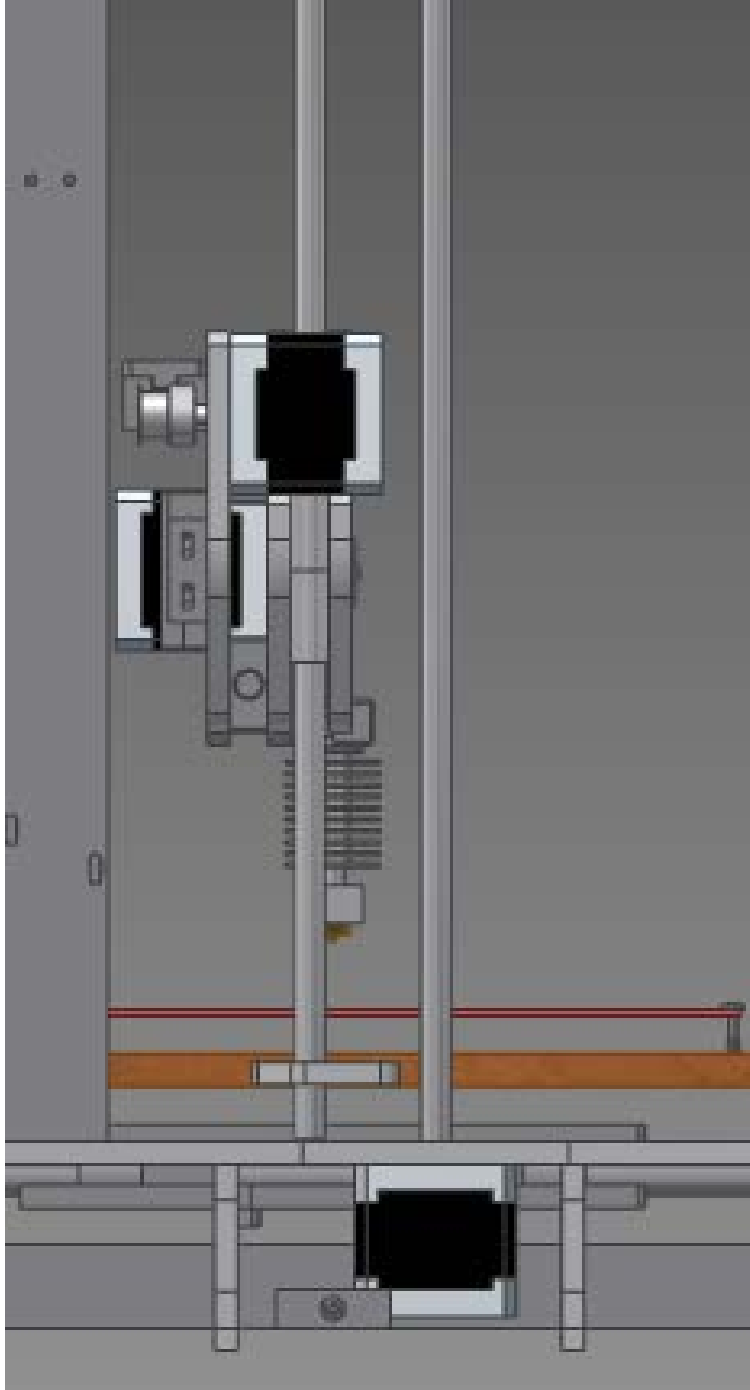
Науч.рук: Юдин Антон Владимирович



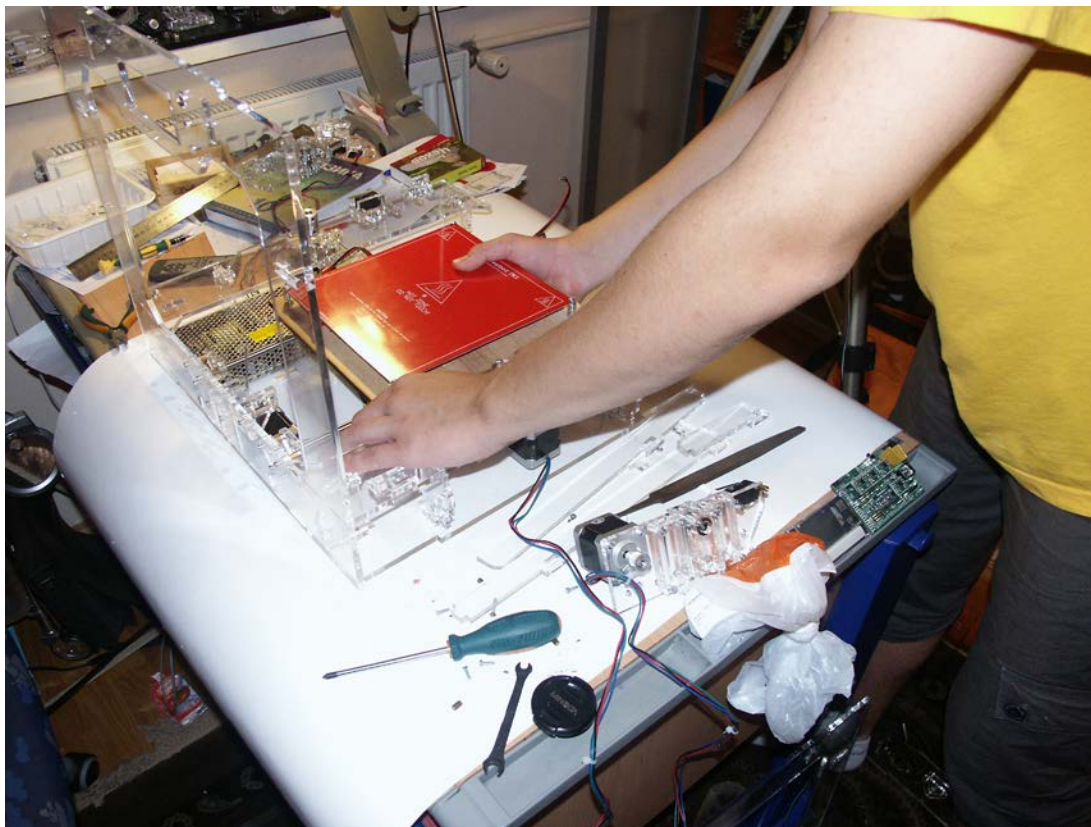
3D-принтер.

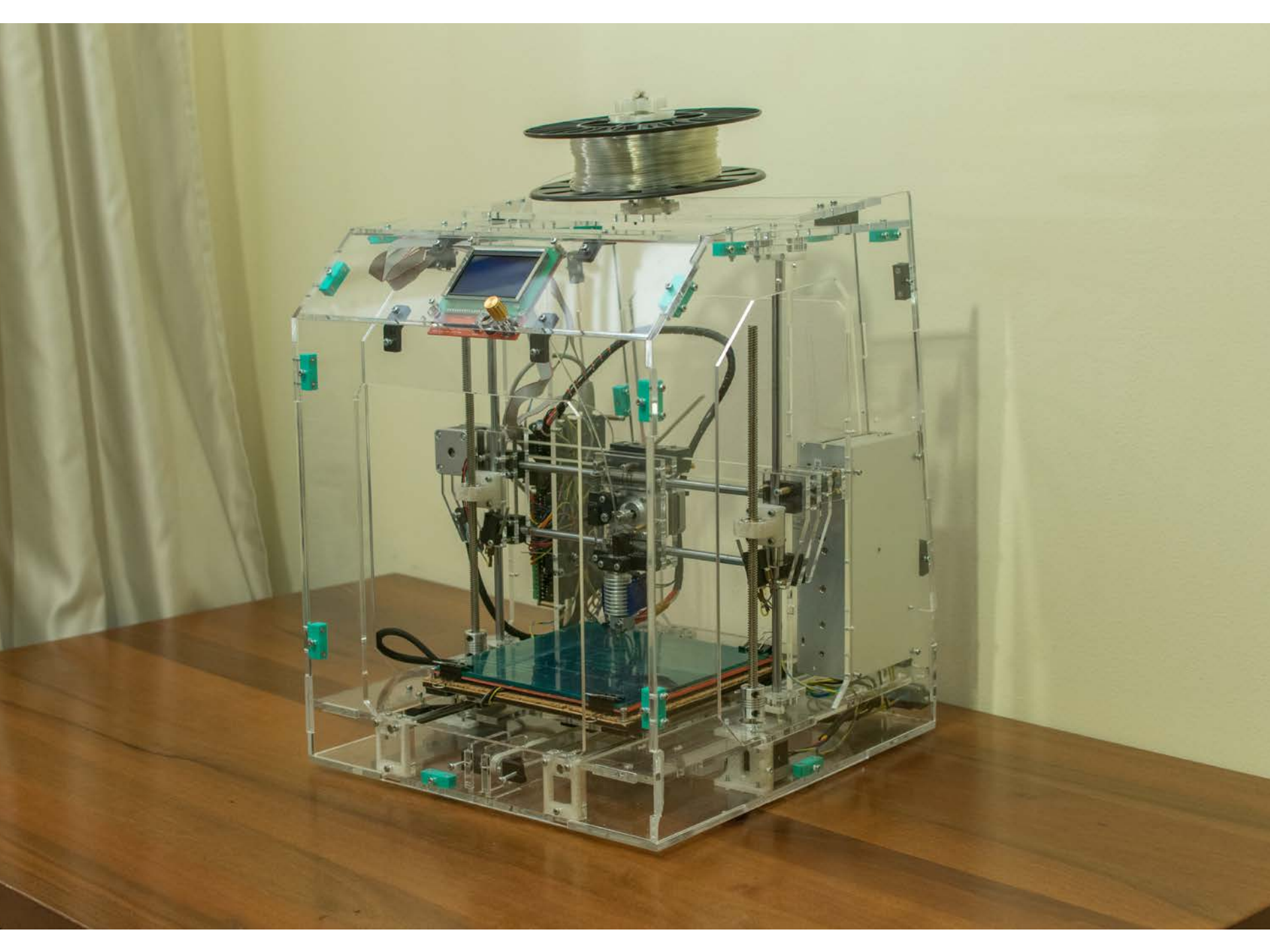




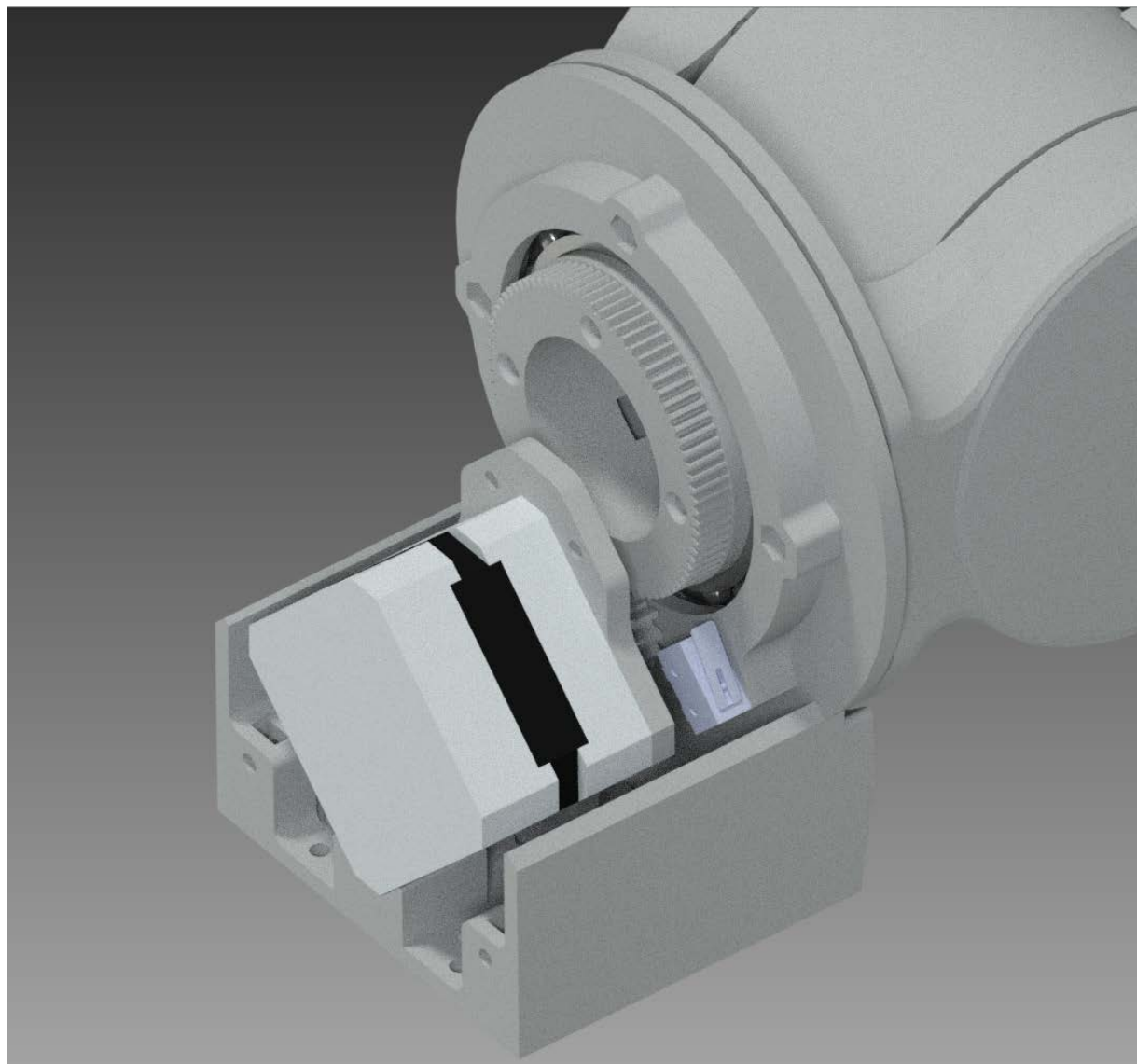


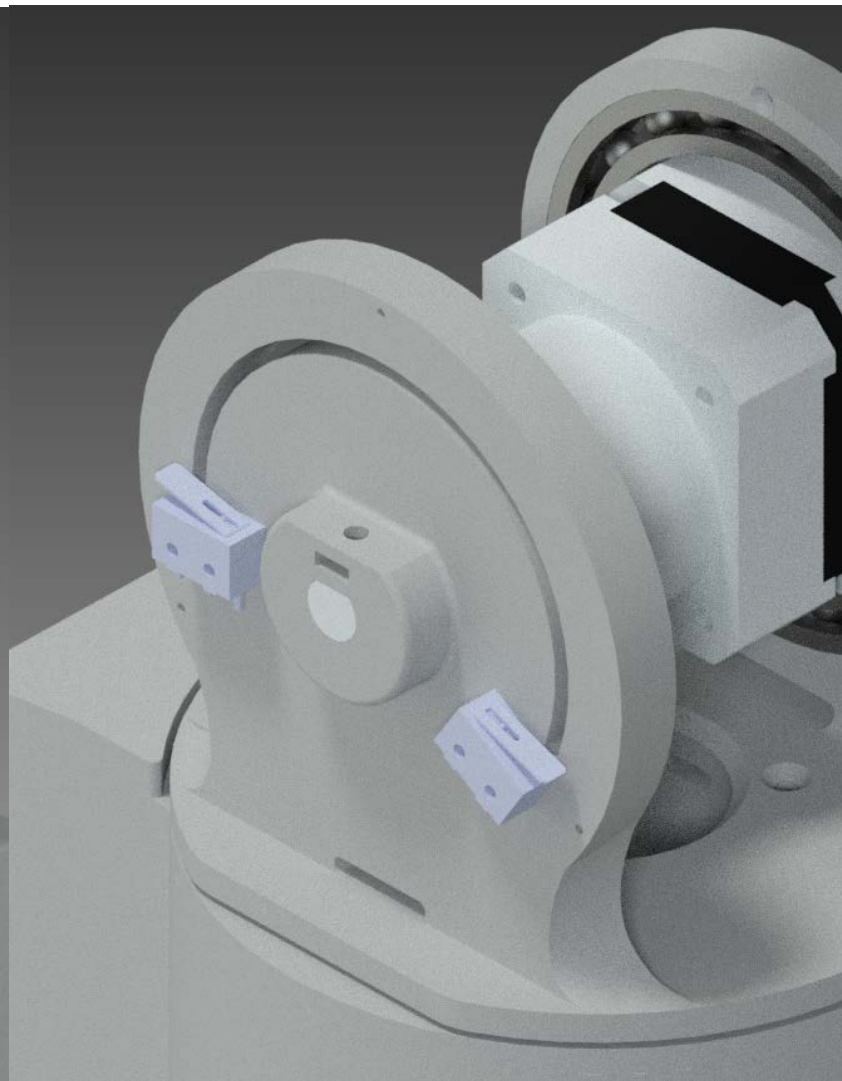
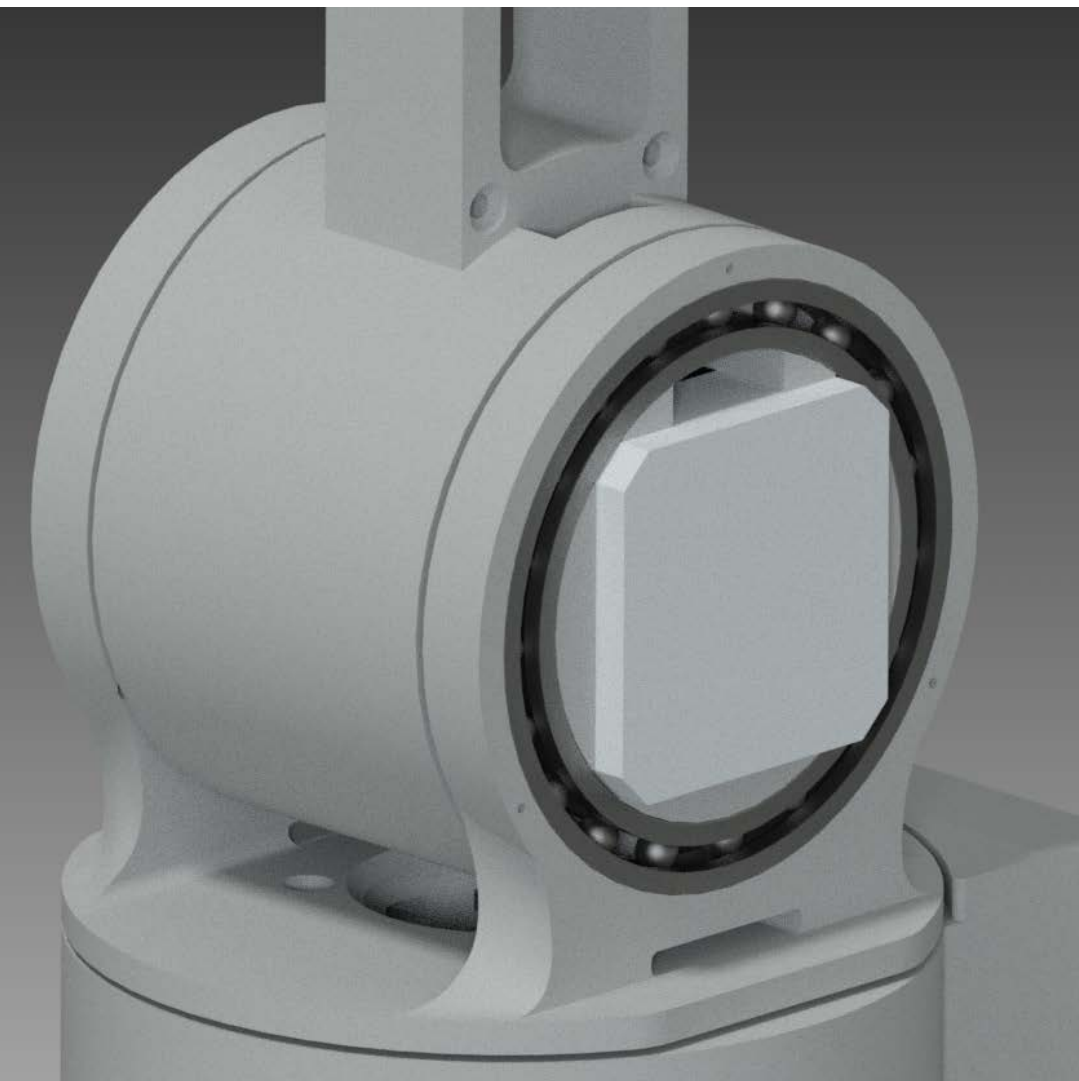
Изготовление станка.

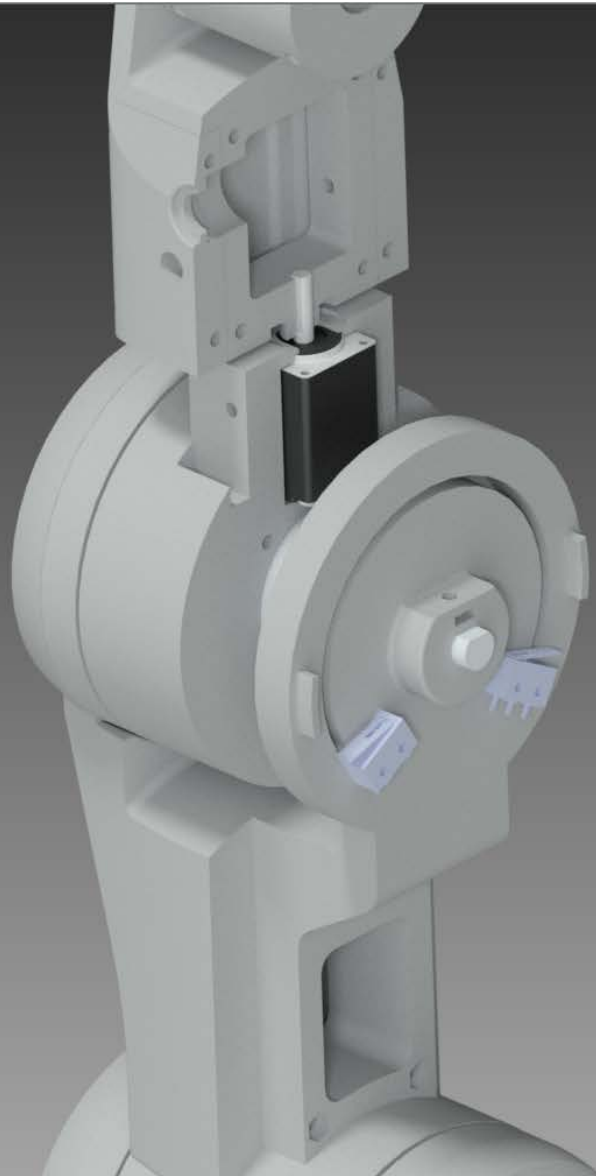


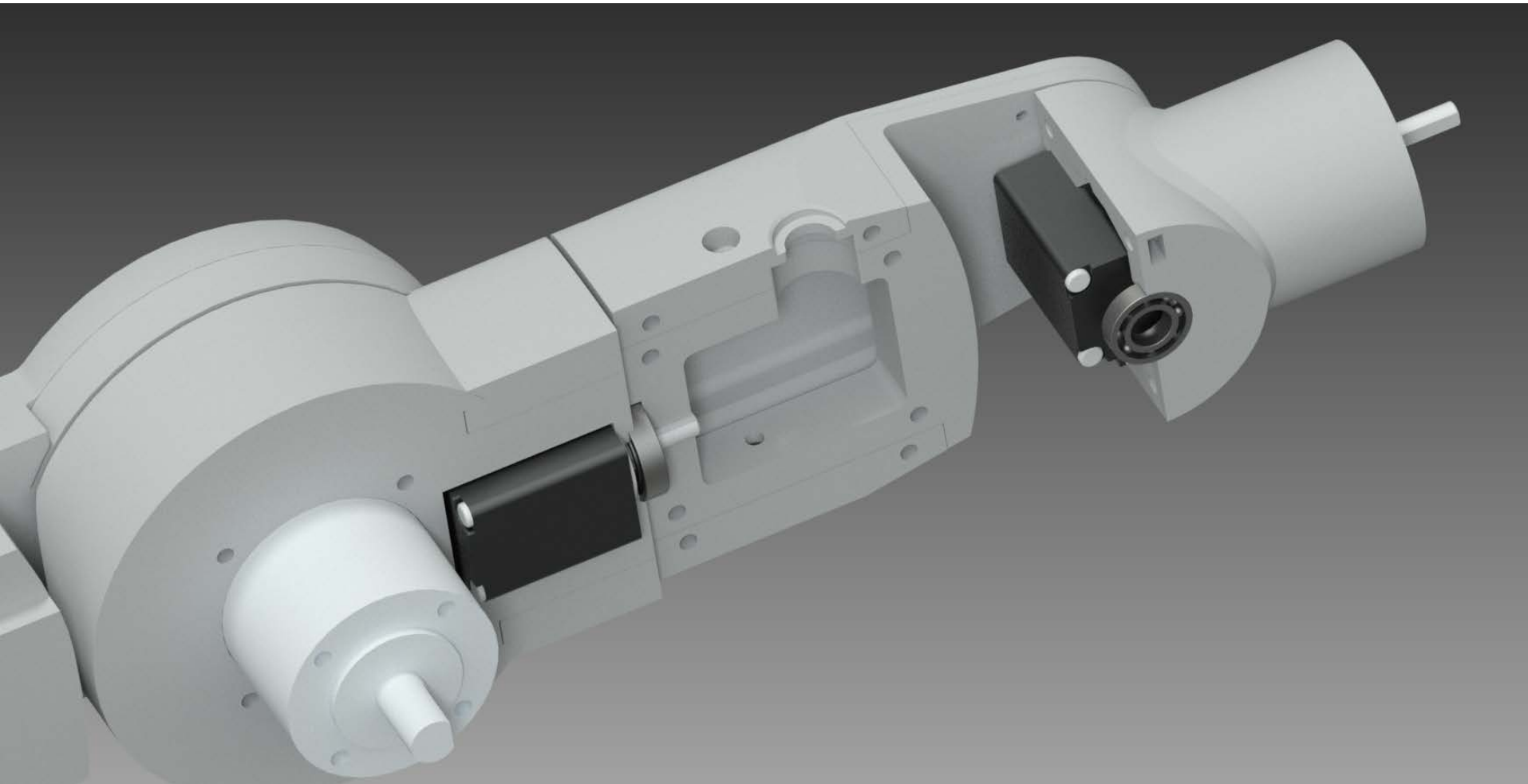


Конструирование манипулятора.

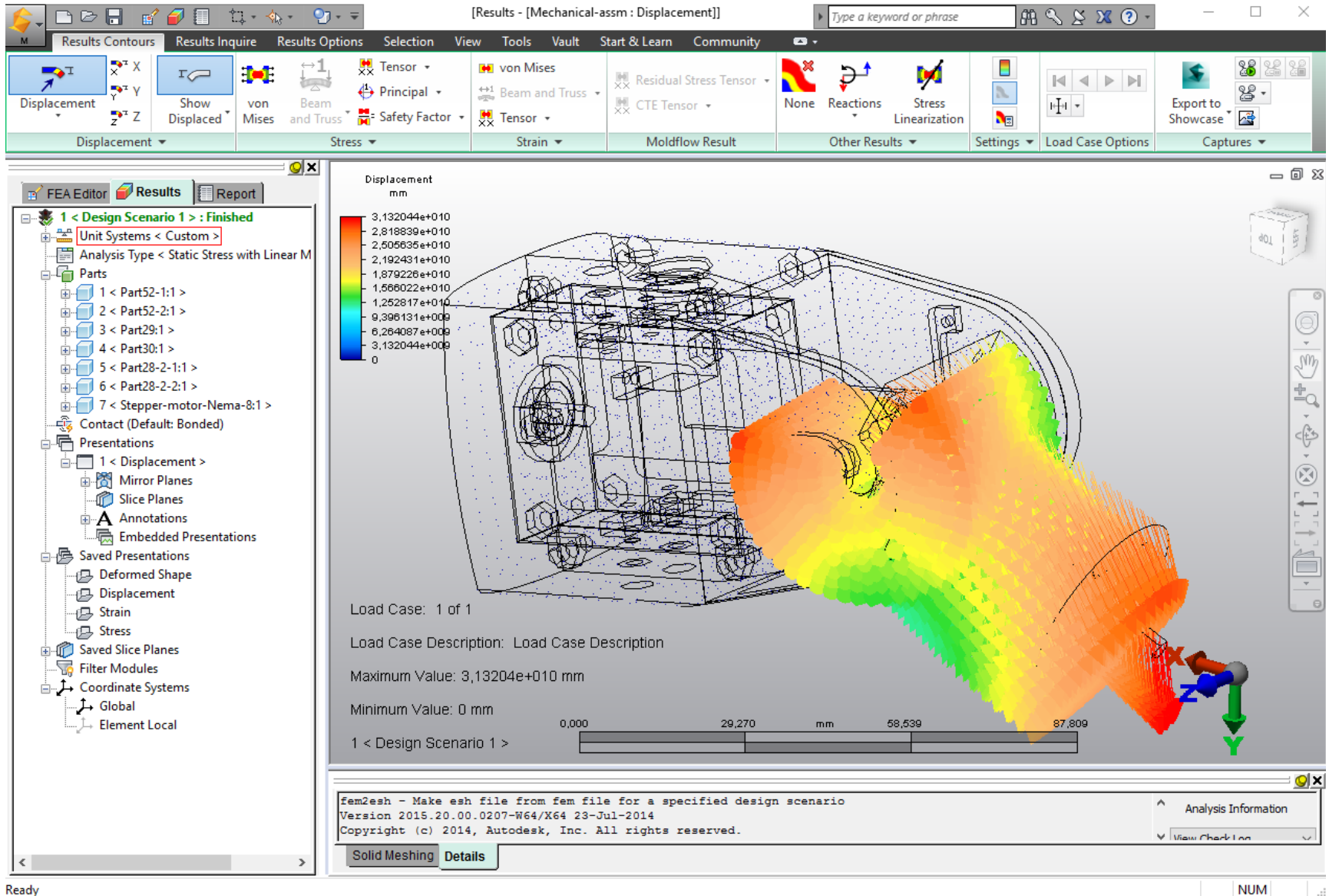








Расчёты.



fem2esh - Make esh file from fem file for a specified design scenario
Version 2015.20.00.0207-W64/X64 23-Jul-2014
Copyright (c) 2014, Autodesk, Inc. All rights reserved.

Analysis Information

View Check Log

Solid Meshing Details

Электроника.

STM32CubeMX STM32F205RBT6-Final.ioc*: STM32F205RBTx

File Project Pinout Window Help

The screenshot displays the STM32CubeMX software interface for configuring the STM32F205RBTx microcontroller. The left sidebar shows the configuration tree for TIM2, TIM3, and TIM4. The main area shows the pinout diagram for the LQFP64 package, with pins labeled for various functions.

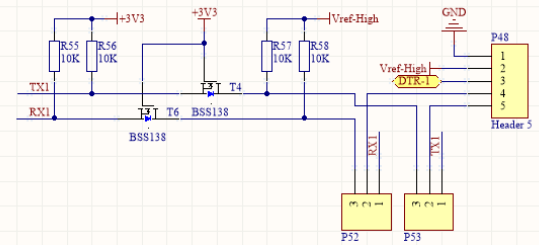
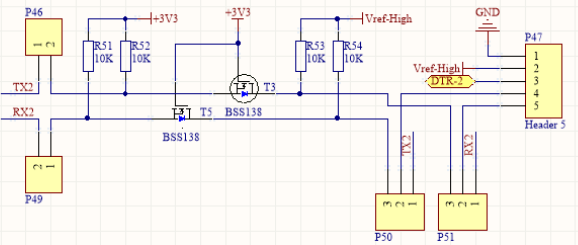
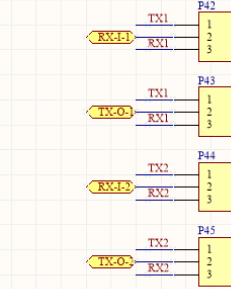
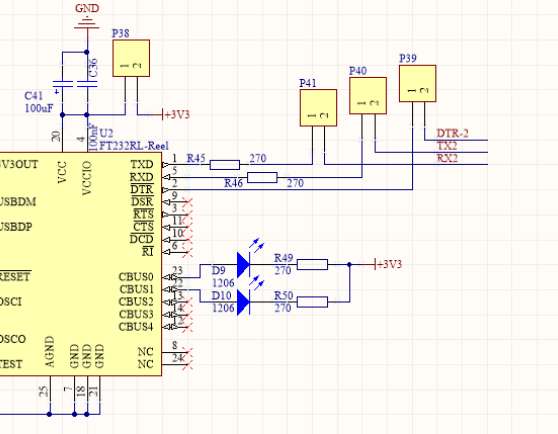
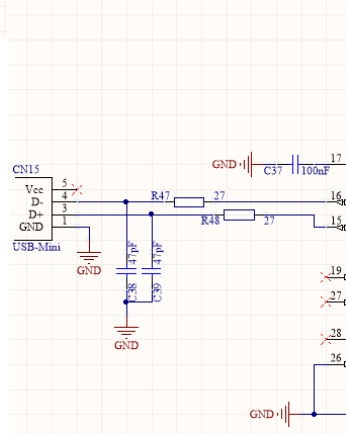
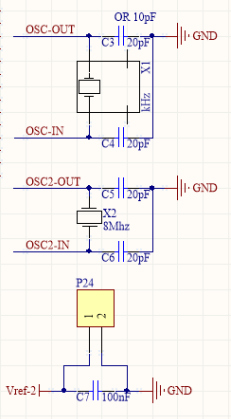
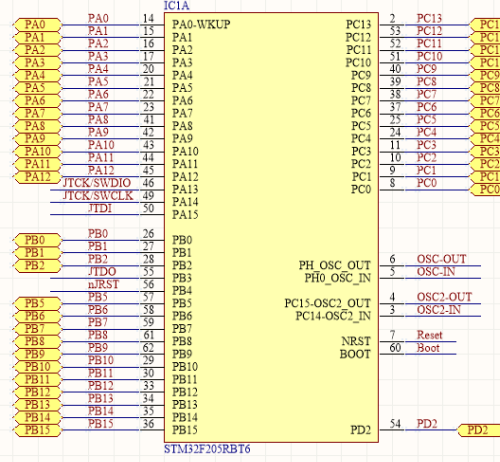
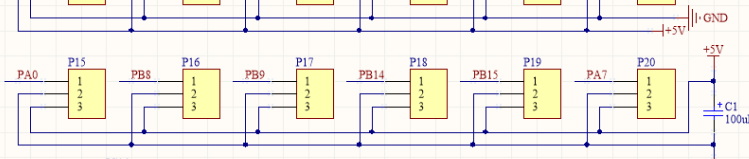
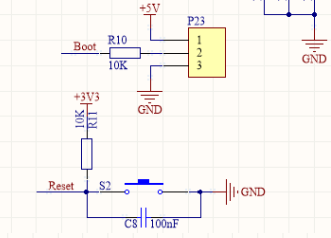
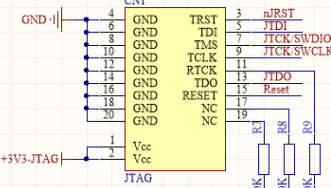
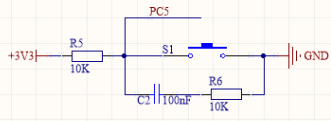
Configuration Tree (Left):

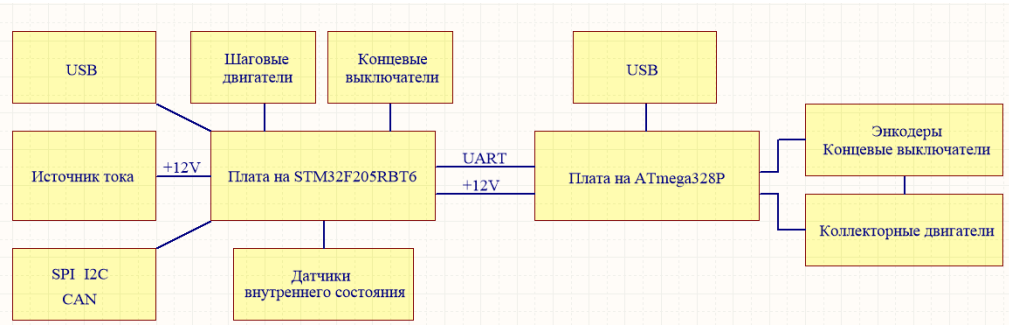
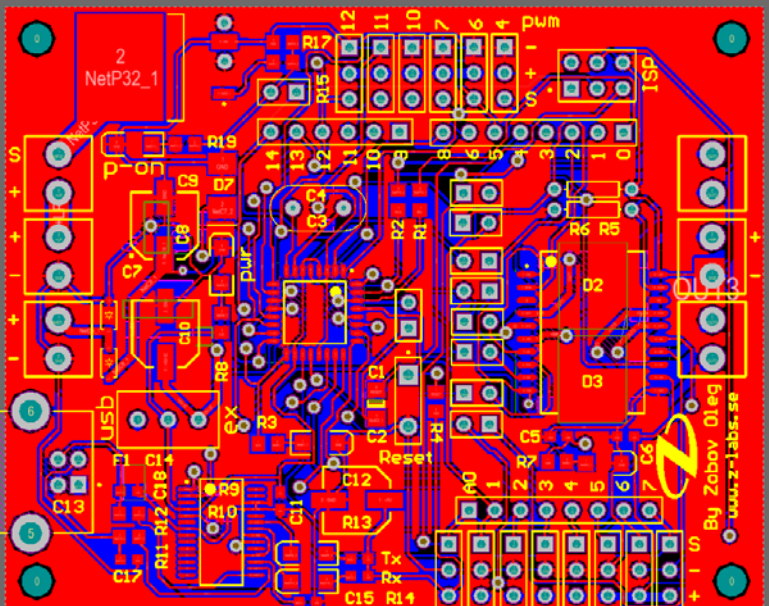
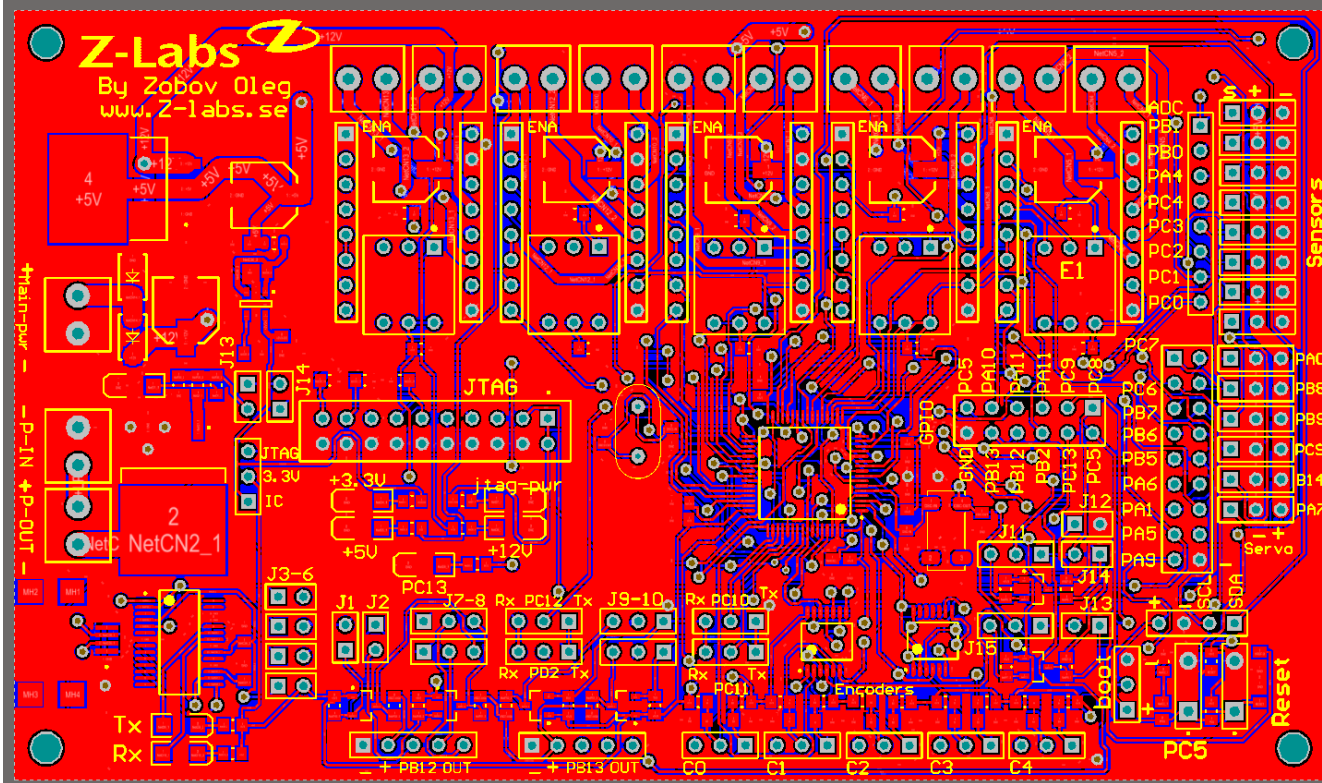
- TIM2:** Channels 3, 4, and Combined Channels are configured in Encoder Mode. Options for Activate-Break-Input, Use ETR as Clearing Source, XOR activation, and One Pulse Mode are disabled.
- TIM3:** Channels 3, 4, and Combined Channels are configured in Encoder Mode. Options for Use ETR as Clearing Source, XOR activation, and One Pulse Mode are disabled.
- TIM4:** Channels 3, 4, and Combined Channels are configured in Encoder Mode. Options for Use ETR as Clearing Source, XOR activation, and One Pulse Mode are disabled.

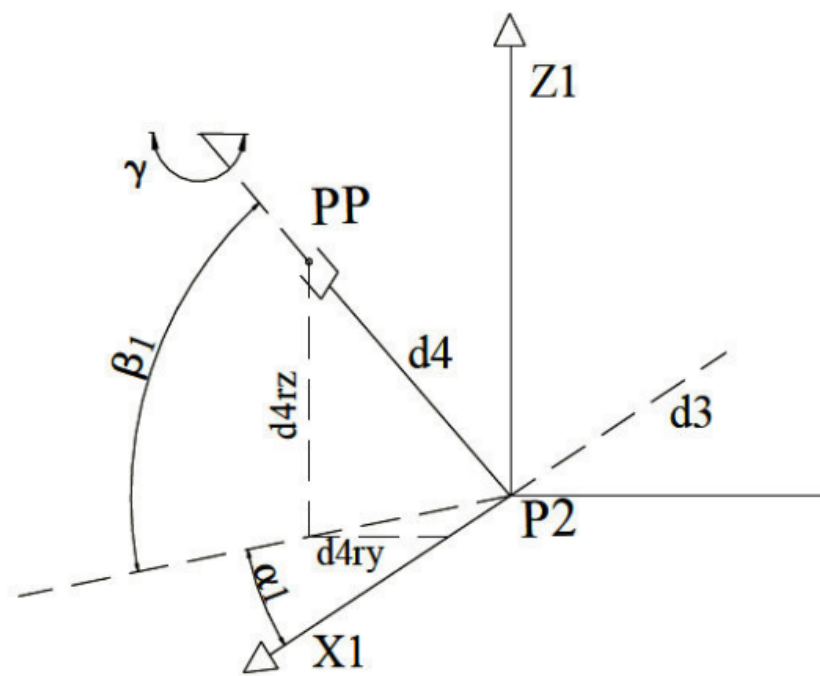
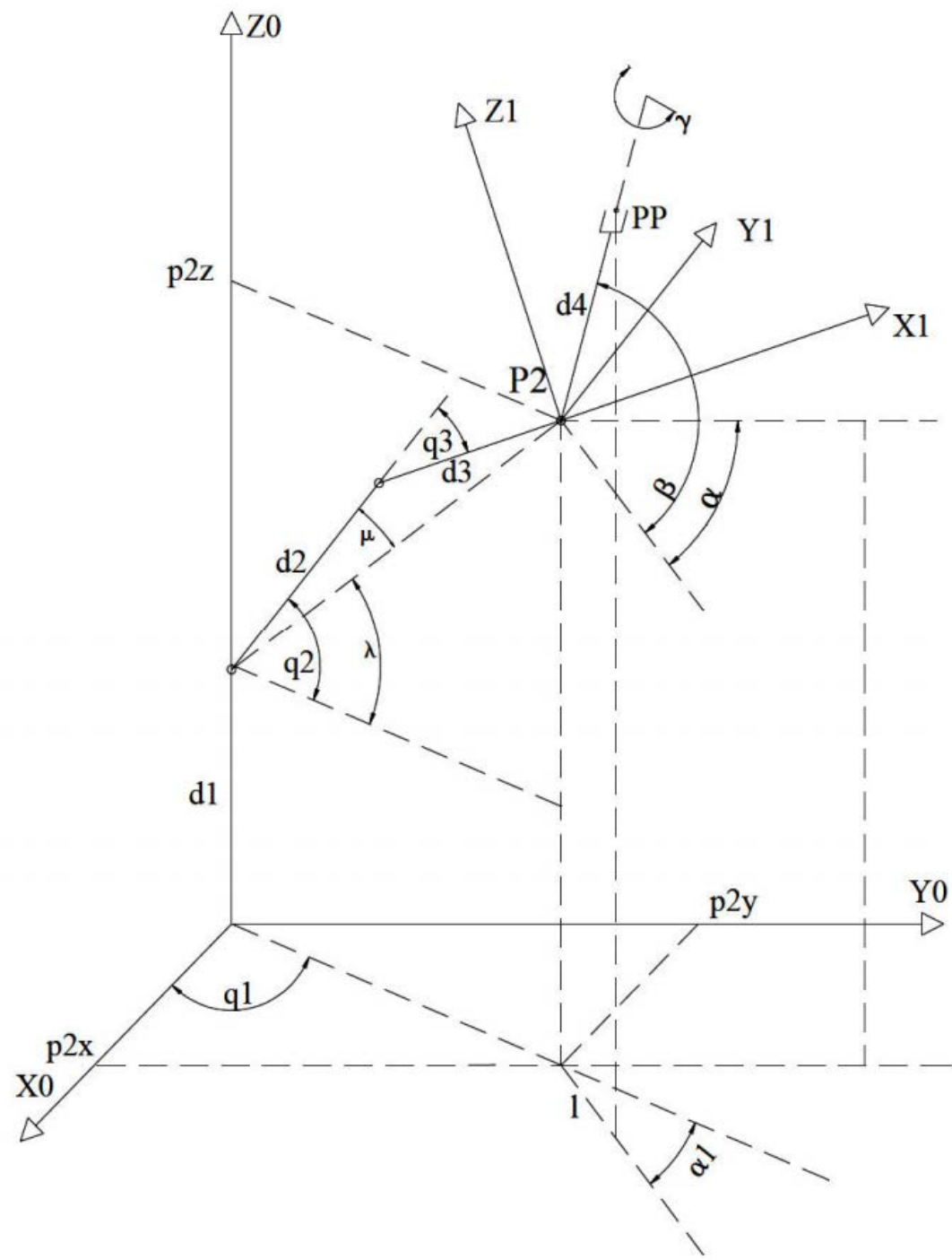
Pinout Diagram (Right):

The diagram shows the STM32F205RBTx LQFP64 package with the following pin assignments:

- Top Row:** VDD, VSS, PB9 (TIM11_CH1), PB8 (TIM10_CH1), BOO..., PB7 (TIM4_CH2), PB6 (TIM4_CH1), PB5 (TIM3_CH2), PB4 (SYS_JTRST), PB3 (SYS_JTDO-SWO), PD2 (UART5_RX), PC12 (UART5_TX), PC10 (UART4_TX), PA15 (SYS_JTDI), PA14 (SYS_JTCK-SWCLK).
- Left Side:** VBAT, GPIO_Output (PC13), RCC_OSC32_IN (PC1...), RCC_OSC32_OUT (PC1...), RCC_OSC_IN (PH0...), RCC_OSC_OUT (PH1...), NRST, ADC1_IN10 (PC0), ADC1_IN11 (PC1), ADC1_IN12 (PC2), ADC1_IN13 (PC3), VSSA, VDDA, TIM5_CH1 (PA0...), TIM2_CH2 (PA1), TIM9_CH1 (PA2).
- Right Side:** VDD, VCA..., PA13 (SYS_JTMS-SWDIO), PA12 (GPIO_Output), PA11 (GPIO_Output), PA10 (GPIO_Output), PA9 (TIM1_CH2), PA8 (TIM1_CH1), PC9 (GPIO_Output), PC8 (GPIO_Output), PC7 (TIM8_CH2), PC6 (TIM8_CH1), PB15 (TIM12_CH2), PB14 (TIM12_CH1), PB13 (GPIO_Output), PB12 (GPIO_Output).
- Bottom Row:** PA3 (TIM9_CH2), VSS, VDD, PA4 (ADC1_IN4), PA5 (TIM2_CH1), PA6 (TIM3_CH1), PA7 (TIM4_CH1), PC4 (ADC1_IN14), PC5 (ADC1_IN15), PB8 (ADC1_IN8), PB1 (ADC1_IN9), PB2 (GPIO_Output), PB10 (12C2_SCL), PB11 (12C2_SDA), VCA..., VDD.







```

C/C++ - Manipulator-board-F205/src/EXTIHelper.h - Eclipse
File Edit Source Refactor Navigate Search Project AVR Run Window Help
Quick Access
Project Explorer
Manipulator-board-F205
  Binaries
  Manipulator-board-F2
  Includes
  src
    write.c
    EXTIHelper.cpp
    EXTIHelper.h
    main.cpp
    ZobovGPIOPort.cpp
    ZobovLimitingSwitch.h
    ZobovLimitingSwitch.cpp
    ZobovManipulator.cpp
    ZobovManipulator.h
    ZobovManipulatorIo.h
    ZobovManipulatorIo.cpp
    ZobovTIM.cpp
  system
    include
      arm
      cmsis
      cortexm
      diag
      stm32f2-stdperiph
        misc.h
        stm32f2xx_adc.h
        stm32f2xx_can.h
        stm32f2xx_crc.h
        stm32f2xx_cryp.h
        stm32f2xx_dac.h
        stm32f2xx_dbg.h
        stm32f2xx_dcm.h
        stm32f2xx_dma.h
        stm32f2xx_exti.h
        stm32f2xx_flash.h
        stm32f2xx_fsmc.h
        stm32f2xx_gpio.h
        stm32f2xx_hash.h
        stm32f2xx_i2c.h
        stm32f2xx_jwdc.h
        stm32f2xx_pwr.h
        stm32f2xx_rcc.h
        stm32f2xx_rng.h
    EXTIHelper.h
8 #ifndef EXTIHELPER_H
9 #define EXTIHELPER_H
10
11 #include "stdlib.h"
12 #include "assert.h"
13
14 #include "stm32f2xx_exti.h"
15
16 //interfacetypedef void(*FunctionPointer)();
17
18
19 @class EXTIIRQn_Interface {
20 public:
21     EXTIIRQn_Interface() {};
22     virtual void IRQn() = 0;
23     virtual ~EXTIIRQn_Interface() {};
24 };
25
26 @class EXTIHelper {
27 public:
28     static const uint8_t EXTI_PortSource[];
29     static const uint8_t EXTI_PinSource[];
30     static const uint8_t EXTI_IRQ[];
31     static const uint8_t EXTI_Line[];
32
33     static EXTIIRQn_Interface *EXTIIRQn[4][16];
34     static char EXTIIRQn_LineLetter[16];
35
36 public:
37     EXTIHelper() {
38         // TODO Auto-generated constructor stub
39     }
40
41
42     static void SetLineLetter(uint8_t pin, uint8_t num) {
43         assert(EXTIIRQn_LineLetter[pin] == -1);
44         EXTIIRQn_LineLetter[pin] = num;
45     }
46
47     static void SetEXTIIRQn(uint8_t pin, uint8_t num, EXTIIRQn_Interface* interface) {
48         assert(EXTIIRQn[num][pin] == NULL);
49         EXTIIRQn[num][pin] = interface;
50     }
51
52     static void CallIRQn(uint8_t pin) {
53         EXTI_ClearFlag(EXTI_Line(pin));
54         //assert(EXTIIRQn[EXTI_IRQn(pin)][EXTI_PortSource(pin)] == NULL);
55     }
56
57 };
58
59 #endif
60

```

